

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. M122-2374		SERIAL NO. Filed Herewith	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT H. Montgomery Manning			
					FILING DATE Filed Herewith		GROUP 2826	
U.S. PATENT DOCUMENTS								
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
an	AA	5,552,628	09/1996	Watanabe				
un	AB	5,585,664	12/1996	Ito				
un	AC	5,583,357	12/1996	Choe				
en	AD	4,418,239	11/1983	Larson				
un	AE	4,693,530	09/1987	Stillie				
un	AF	4,781,620	11/1988	Tengler				
on	AG	4,933,743	06/1990	Thomas				
on	AH	5,000,818	03/1991	Thomas				
un	AI	5,117,276	05/1992	Thomas				
un	AJ	5,123,325	06/1992	Turner				
on	AK	5,176,538	01/1993	Hensell, III				
on	AL	4,686,759	08/1987	Pala				
FOREIGN PATENT DOCUMENTS								
	Document Number	Date	Country	Class	Subclass	Translation		
						Yes	No	
un	AM	JP 0097300	04/1994	Japan				
un	AN	JP 054925	02/1990	Japan				
	AO							
	AP							
	AQ							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AR	Thomas, Michael E., "VLSI Multilevel Micro-Coaxial Interconnects for High Speed Devices". 1990, IEEE, pps. 55-58.						
	AS							
	AT							
EXAMINER				DATE CONSIDERED				
on				5/28/05				
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*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
ON	AA	4,922,323	Potter				
ON	AB	5,323,048	Onuma				
UN	AC	5,329,155	Lao				
UN	AD	5,471,093	Cheung				
UN	AE	5,028,981	Eguchi				
UN	AF	4,780,394	Blanchard				
UN	AG	5,656,543	Chung				
UN	AH	5,654,917	Ogura				
UN	AI	5,519,239	Chu				
UN	AJ	5,413,961	Kim				
UN	AK	5,451,551	Krishnan et al				
UN	AL	5,956,615	Nguyen et al				

  

FOREIGN PATENT DOCUMENTS							
Document Number	Date	Country	Class	Subclass	Translation		
					Yes	No	
AM							
AN							
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)			
AR			
AS			
AT			

  

EXAMINER <i>ON</i>	DATE CONSIDERED <i>5/28/04</i>
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